

# NS-SFP+W45L80D NS-SFP+W54L80D

RoHS Compliant 10Gb/s SFP+ BiDi 80km Optical Transceiver TX1490nm/RX1550nm,TX1550nm/RX1490nm

# **FEATURES**

- ◆ SFP MSA package with single LC connector
- ◆ Typical bidi 1490/1550nm and 1550nm/1490nm for commission
- APD receiver for high sensitivity
- ◆ Very low EMI and excellent ESD protection
- ◆ Digital Diagnostic Monitor Interface
- Hot pluggable
- ◆ Support 9.95Gb/s to 11.1Gb/s serial optical interface
- Up to 80km distance
- ◆ Compliant with SFP+ MSA
- ◆ High transmission margin
- ◆ +3.3V single power supply
- ◆ Below <1.5W power consumption

### **APPLICATIONS**

- ◆ 10GBASE-BX at 10.3125 Gb/s
- ◆ Other optical link

### **STANDARD**

- ◆ SFF-8472 reversion 9.5 compliant
- ◆ IEEE802.3-2005 compliant
- ◆ Telcordia GR-468-CORE compliant
- ◆ FCC 47 CFR Part 15, Class B compliant
- ◆ FDA 21 CFR 1040.10 and 1040.11, class1 compliant
- ◆ RoHS compliant

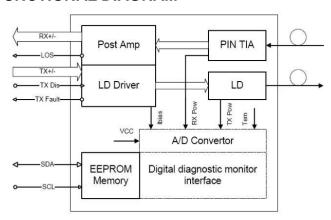
### **DESCRIPTIONS**

Newnets NS-SFP+W45L80D NS-SFP+W54L80D is hot pluggable 3.3V Small-Form-Factor transceiver module. It designed expressly for high-speed communication applications that require rates up to 11.1Gbps,it designed to be compliant with SFF-8472 and SFP+ MSA. The module data link up to 80km in 9/125um single mode fiber. The optical output can be disabled by a LVTTL logic high-level input of Tx Disable. Tx Fault is provided to indicate that degradation of the laser. Loss of signal (LOS) output is provided to indicate the loss of an input optical signal of receiver or the link status with partner.

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### **FUNCTIONAL DIAGRAM**



### **ABSOLUTE MAXIMUM RATINGS**

| Parameter              | Symbol | Min. | Max. | Unit | Note |
|------------------------|--------|------|------|------|------|
| Supply Voltage         | Vcc    | -0.5 | 4.0  | V    |      |
| Storage<br>Temperature |        | -40  | 85   | °C   |      |
| Relative Humidity      |        |      | 85   | %    |      |

Note: Stress in excess of the maximum absolute ratings can cause permanent damage to the module

### **GERERAL OPERATING CHARACTERISTICS**

| Parameter            | Symbol        | Min. | Тур     | Max. | Unit | Note |
|----------------------|---------------|------|---------|------|------|------|
| Data Rate            | Ethernet      |      | 10.3125 |      | Gb/s |      |
|                      | Fiber Channel |      | 9.953   |      | Gb/s |      |
| Supply Voltage       | Vcc           | 3.13 | 3.3     | 3.47 | V    |      |
| Supply Current       | Icc₅          |      |         | 500  | mA   |      |
| Operating Case Temp. | Тс            | 0    |         | 70   | °C   |      |

### **ELECTRICAL INPUT/OUTPUT CHARACTERISTICS**

#### **Transmitter**

| Parameter                 | Symbol | Min. | Тур | Max.    | Unit | Note |
|---------------------------|--------|------|-----|---------|------|------|
| Diff. input voltage swing |        | 120  |     | 820     | m∨pp | 1    |
| Tx Disable input H        | VIH    | 2.0  |     | Vcc+0.3 | V    |      |
| Tx Disable input L        | VIL    | 0    |     | 0.8     | V    |      |
| Tx Fault output H         | VOH    | 2.0  |     | Vcc+0.3 | V    | 2    |
| Tx Fault output L         | VOL    | 0    |     | 0.8     | V    |      |
| Input Diff. Impedance     | Zin    |      | 100 |         | Ω    |      |

#### Receiver

| Parameter                  | Symbol | Min. | Тур | Max.    | Unit | Note |
|----------------------------|--------|------|-----|---------|------|------|
| Diff. output voltage swing |        | 340  | 650 | 800     | mVpp | 3    |
| Rx LOS Output H            | VOH    | 2.0  |     | Vcc+0.3 | V    | 2    |
| Rx LOS Output L            | VOL    | 0    |     | 0.8     | V    | 2    |

Note 1) TD+/- are internally AC coupled with  $100\Omega$  differential termination inside the module.

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Note 2) Tx Fault and Rx LOS are open collector outputs, which should be pulled up with 4.7k to  $10k\Omega$  resistors on the host board. Pull up voltage between 2.0V and Vcc+0.3V.

Note 3) RD+/- outputs are internally AC coupled, and should be terminated with 100Ω (differential) at the user SERDES



### OPTICAL CHARACTERISTICS

Transmitter (0~70°C@10.3125Gb/s)

| Parameter                    | Symbol               | Min. | Тур       | Max. | Unit | Note |
|------------------------------|----------------------|------|-----------|------|------|------|
| Operating Wavelength         |                      |      | 1490/1550 |      | nm   | 1    |
| Ave. output power (Enabled)  | Ро                   | +1   |           | +6   | dBm  | 2    |
| Extinction Ratio             | ER                   | 5    |           |      | dB   | 2    |
| RMS spectral width           | Δλ                   |      |           | 1    | nm   |      |
| Rise/Fall time (20%~80%)     | Tr/Tf                |      |           | 50   | ps   | 3    |
| Optical modulation amplitude | OMA                  | -4.8 |           |      | dBm  |      |
| Dispersion penalty           |                      |      |           | 3    | dB   |      |
| Output Optical Eye           | IEEE 802.3-2005 Comp |      |           |      |      |      |

### Receiver (0~70°C@10.3125Gb/s)

| Parameter               | Symbol | Min. | Тур       | Max. | Unit | Note |
|-------------------------|--------|------|-----------|------|------|------|
| Operating<br>Wavelength |        |      | 1550/1490 |      | nm   | 1    |
| Sensitivity             | Psen   |      |           | -22  | dBm  | 4    |
| Min. overload           | Pimax  | -7   |           |      | dBm  |      |
| LOS Assert              | Pa     | -40  | dBm       |      |      |      |
| LOS De-assert           | Pd     |      |           | -22  | dBm  |      |
| LOS Hysteresis          | Pd-Pa  | 0.5  |           | 4    | dB   |      |

Note 1) TX1490nm/RX1550nm,TX1550nm/RX1490nm.

Note 2) Measured at 10.3125b/s with PRBS 231- 1 NRZ test pattern.

Note 3) 20%~80%

Note 4) Under the ER worst case, measured at 10.3125 Gb/s with PRBS  $2_{31}$  - 1 NRZ test pattern for BER < 1x10 $_{-}$ 12

### SERIAL INTERFACE FOR ID AND DDM

#### • Serial Interface for ID and DDM

The SFP modules implement the 2-wire serial communication protocol as defined in the SFP MSA.

The serial ID information of the SFP modules and Digital Diagnostic Monitor parameters can be accessed through the I2C interface at address A0h and A2h. The memory is mapped in Table 1. Detailed ID information(A0h) And the DDM specification(A2h). For more details of the memory map and byte definitions, please refer to the SFF-8472 (Rev 9.3, Aug. 2002), "Digital Diagnostic Monitoring Interface for Optical Transceivers".

The DDM parameters have been internally calibrated.

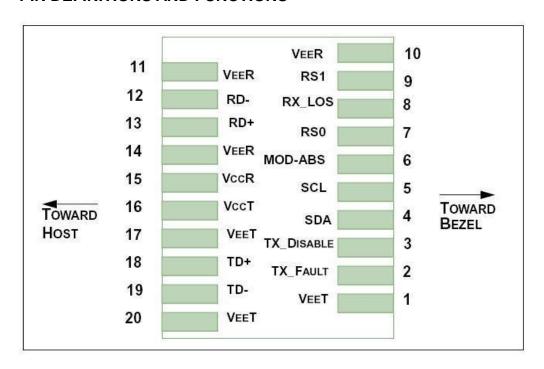
Table 1. Digital Diagnostic Memory Map (Specific Data Field Descriptions)

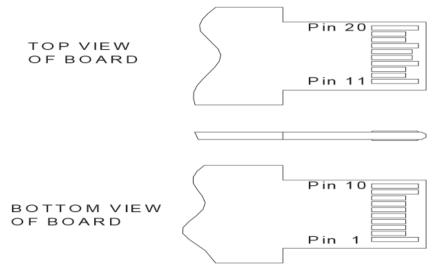
| 2                   | wire address 1010000X (A0h)              | 2 wire address 1010001X (A2h) |   |  |
|---------------------|--|-------------------------------|---|--|
| Address Information |  | Address                       | Information                               |  |
| 0~95 Serial ID      | Contail ID Defend by CED MCA (OC batter) | 0~55                          | Alarm and Warning Thresholds (56 bytes)   |  |
|                     | Serial ID Defined by SFP MSA (96 bytes)  | 56~95                         | Calibration Constants (40 bytes)          |  |
|                     |  | 96~119                        | Real Time Diagnostic Interface (24 bytes) |  |
| 96~127              | Vendor Specific (32 bytes)               | 120~127                       | Vender Specific (8 bytes)                 |  |
| 128~255             | D 1 0550070 (400 b. 4)                   | 128~247                       | User Writable EEPROM (120 bytes)          |  |
|                     | Reserved,SFF8079 (128 bytes)             | 248~255                       | Vender Specific (8 bytes)                 |  |

E-mail: info@newnets.ru



## PIN DEFINITIONS AND FUNCTIONS





E-mail: info@newnets.ru



| PIN# | Name       | Function  | Notes  |  |  |  |
|------|------------|---|--------|--|--|--|
| 1    | VeeT       | Module transmitter ground   | Note1  |  |  |  |
| 2    | Tx Fault   | Module transmitter fault  |        |  |  |  |
| 3    | Tx Disable | Transmitter Disable; Turns off transmitter laser output   | Note 3 |  |  |  |
| 4    | SDL        | 2 wire serial interface data input/output (SDA)   |        |  |  |  |
| 5    | SCL        | 2 wire serial interface clock input (SCL)   |        |  |  |  |
| 6    | MOD-ABS    | Module Absent, connect to VeeR or VeeT in the module  | Note 2 |  |  |  |
| 7    | RS0        | Rate select0,optionally control SFP+ receiver. When high, input data rate >4.5Gb/s;when low, input data rate <=4.5Gb/s    |        |  |  |  |
| 8    | LOS        | Receiver Loss of Signal Indication  | Note4  |  |  |  |
| 9    | RS1        | Rate select0,optionally control SFP+ transmitter. When high, input data rate >4.5Gb/s;when low, input data rate <=4.5Gb/s |        |  |  |  |
| 10   | VeeR       | Module receiver ground  | Note 1 |  |  |  |
| 11   | VeeR       | Module receiver ground  |        |  |  |  |
| 12   | RD-        | Receiver inverted data out put  |        |  |  |  |
| 13   | RD+        | Receiver non-inverted data out put  |        |  |  |  |
| 14   | VeeR       | Module receiver ground  | Note 1 |  |  |  |
| 15   | VccR       | Module receiver 3.3V supply   |        |  |  |  |
| 16   | VccT       | Module transmitter 3.3V supply  |        |  |  |  |
| 17   | VeeT       | Module transmitter ground   | Note 1 |  |  |  |
| 18   | TD+        | Transmitter inverted data out put   |        |  |  |  |
| 19   | TD-        | Transmitter non-inverted data out put   |        |  |  |  |
| 20   | VeeT       | Module transmitter ground   | Note1  |  |  |  |

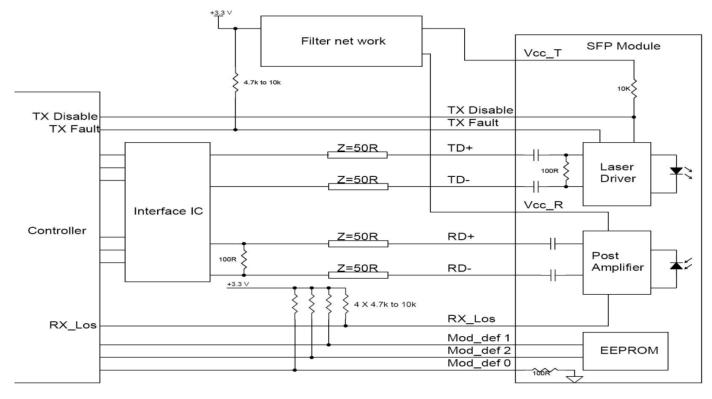
Note 1) The module ground pins shall be isolated from the module case.

Note 2) This pin is an open collector/drain output pin and shall be pulled up with 4.7K-10Kohms to Host\_Vcc on the host board.

Note 3) This pin shall be pulled up with 4.7K-10Kohms to VccT in the module.

Note 4) This pin is an open collector/drain output pin and shall be pulled up with 4.7K-10Kohms to Host\_Vcc on the host board. In FC designated as RX\_LOS, inSONET designated as LOS, and in Ethernet designated at Signal Detect

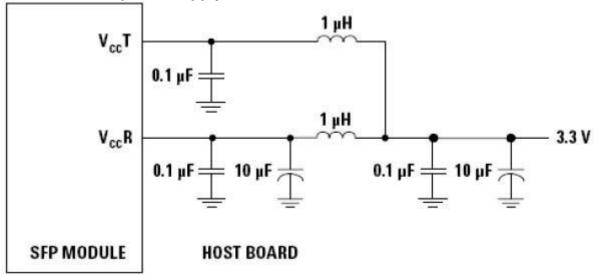
### TYPICAL INTERFACE CIRCUIT



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ote: Inductors with DC resistance of less than 1Ω should be used in order to maintain the required voltage at the SFP input pin with 3.3V supply voltage. When the recommended supply filtering network is used, hot plugging of the SFP transceiver module will result in an inrush current of no more than 30 mA greater than the steady state value

### **RDERING INFORMATION**

| Part Number    | Description  |
|----------------|--|
| NS-SFP+W45L80D | SFP+ BiDi,T1490/R1550,10.3125Gbps, 80KM,0~70°C, with DDM |
| NS-SFP+W54L80D | SFP+ BiDi,T1550/R1490,10.3125Gbps, 80KM,0~70°C, with DDM |

E-mail: info@newnets.ru

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